IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **PARENT et al.**	
Serial No.: **10/799,742**	Group Art Unit: **2827**
Filed: **March 12, 2004**	Examiner: **Mai, Son Luu**
Title: **Method and Circuit for Reducing Defect Current From Array Element Failures in Random Access Memories**	
Attorney Docket No.: **CD03011**	

The entry is denied by Examiner SM

RESPONSE TO FINAL OFFICE ACTION

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

A. Introductory Comments

The following is submitted in response to the FINAL Office Action dated October 31, 2005. The two month Advisory Action period ends January 3, 2006.

37 C.F.R. §1.8

I hereby certify that this correspondence is being

[X] transmitted via facsimile to the United States Patent and Trademark Office to fax number: 1-571-273-8300
Date of Transmittal: DECEMBER 23, 2005

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